

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	693	multiphase adj1 clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 14:16
L2	18	(multiphase adj1 clock) with (phase adj1 select\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 14:19
L3	1	(multiphase adj1 clock) with (phase adj1 select\$3) and ("360/N")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 14:20
L5	5	(multiphase adj1 clock) with (phase adj1 select\$3) and (degree\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 14:22
L6	78	(multiphase adj1 clock) and (phase adj1 select\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 14:22
L7	7	(multiphase adj1 clock) and (phase adj1 select\$3) and ("360")	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/05/26 14:22
S1	0	(multiphase adj1 clock adj1 generator) and (phase adj1 offset\$1) and degree\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 09:12
S2	2	(multiphase adj1 clock adj1 generator) and (phase adj1 offset\$1) and degrees\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 09:13
S3	2	(multiphase adj1 clock adj1 generator) and (phase adj1 offset\$1) and degree\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 09:16

S4	5	((multiphase or multi-phase)adj1 clock adj1 generator) and (phase adj1 offset\$1) and degree\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 09:46
S5	3	"09879960"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 09:48
S6	62	"4584695"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 09:51
S7	20	"4584695" and degree\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 09:52
S8	11	"4584695" and phase adj1 offset	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 10:21
S9	2	"20020136286"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 10:21
S10	3320	375/371	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/21 09:11
S11	32	USB adj1 receiver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 11:36
S12	1	USB adj1 receiver and phase adj1 detect\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 11:36
S13	80	phase adj1 detect\$3 and multiphase adj1 clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 14:36

S14	22	phase adj1 detect\$3 and multiphase adj1 clock and phase adj1 select\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 11:50
S15	4	phase adj1 detect\$3 and multiphase adj1 clock and phase adj1 select\$3 and shift adj1 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 14:35
S16	1	phase adj1 detect\$3 and multiphase adj1 clock and phase adj1 select\$3 and bidirectional adj1 shift adj1 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 11:53
S17	0	phase adj1 detect\$3 and multiphase adj1 clock and phase adj1 select\$3 and bidirectional adj1 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 11:53
S18	1	USB and phase adj1 detect\$3 and multiphase adj1 clock	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:00
S19	32	USB adj1 receiver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:29
S20	2	USB adj1 receiver and signal adj1 recovery	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:11
S21	8548	signal adj1 recover\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:35
S22	1	signal adj1 recover\$3 with bidirectional adj1 shift adj1 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:35
S23	3	signal adj1 recover\$3 and bidirectional adj1 shift adj1 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:41

S24	380	bidirectional adj1 shift adj1 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:41
S25	912915	bidirectional adj1 shift adj1 register and phase detector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:41
S26	14	bidirectional adj1 shift adj1 register and phase adj1 detector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:44
S27	6	phase adj1 detector and multiphase adj1 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 13:45
S28	26	phase adj1 detector and multiphase adj1 clock adj1 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 14:30
S29	33	four adj1 clock adj1 period	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 15:33
S30	0	bidirectional adj1 rotator and phase adj1 detector and multiphase adj1 clock adj1 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 14:34
S31	3	bidirectional adj1 rotator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 14:34
S32	70	multiphase adj1 clock and phase adj1 detector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 14:36
S33	9	multiphase adj1 clock and phase adj1 detector and phase adj2 selector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 14:36

S34	7	multiphase adj1 clock and phase adj1 detector and phase adj1 selector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 14:46
S35	70	multiphase adj1 clock and phase adj1 detector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 15:35
S36	24	"5834987"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/18 15:35
S37	935	phase adj1 selector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 10:04
S38	78	phase adj1 selector and multiphase	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 10:05
S39	12	phase adj1 selector and multiphase and (shifter or rotator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 10:09
S40	1	four-clock adj1 period and recovery	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 10:11
S41	0	four-clock adj1 period and phase adj1 selector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 10:11
S42	13	four-clock adj1 period	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 10:11
S43	0	four-clock adj1 period and clock adj2 recovery	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 10:11

S44	338	multiphase adj2 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 15:34
S45	7	multiphase adj2 generator and phase adj1 (detector and selector)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 16:50
S46	14628	(first and second) adj1 control adj1 signal	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 16:51
S47	2	(first and second) adj1 control adj1 signal and multiphase adj2 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 16:52
S48	561	(bidirectional or bi-directional) adj1 shift adj1 register	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 16:52
S49	218	(bidirectional or bi-directional) adj1 shift adj1 register and phase	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 16:52
S50	10	(bidirectional or bi-directional) adj1 shift adj1 register and phase adj1 selector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/19 16:53
S51	148	USB adj1 transceiver	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 09:09
S52	0	USB adj1 transceiver and (multiphase or multi-phase) adj2 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 09:10
S53	0	(USB adj1 transceiver) and ((multiphase or multi-phase) adj2 generator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 09:10

S54	0	(USB adj1 receiver) and ((multiphase or multi-phase) adj2 generator)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 09:12
S55	32	(USB adj1 receiver)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 09:12
S56	0	"samsung.as"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 10:33
S57	6	multiphase adj1 clock adj1 generator and phase adj1 selector and phase adj1 detector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 09:54
S58	11	(multiphase or multi-phase)adj1 clock adj1 generator and phase adj1 selector and phase adj1 detector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 09:54
S59	147502	samsung.as.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 10:37
S60	4	samsung.as. and (multiphase or multi-phase) adj1 clock adj1 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 10:38
S61	11	samsung.as. and phase adj1 selector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 10:41
S62	1436	clock adj2 data adj1 recovery	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 10:42
S63	26	clock adj2 data adj1 recovery and universal adj1 serial adj1 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 11:10

S64	309	(multitphase or multi-phase) adj2 generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 11:10
S65	55	(multitphase or multi-phase) adj2 generator and selector	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 11:30
S66	2	(multitphase or multi-phase) adj2 generator and selector and universal adj1 serial adj1 bus	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 13:29
S67	10	"5850422"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 15:53
S68	3	"5850422" and transmitter	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 16:27
S69	5	"5850422" and generator	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/10/20 16:27